

L Number	Hits	Search Text	DB	Time stamp
3	356	711/105.ccls.	USPAT; US-PGPUB	2002/03/05 17:52
4	622	710/305-306.ccls.	USPAT; US-PGPUB	2002/03/05 17:53
5	186	710/310.ccls.	USPAT; US-PGPUB	2002/03/05 17:54
6	3	711/105.ccls. and 710/305-306.ccls.	USPAT; US-PGPUB	2002/03/05 17:54
7	58312	(microprocessor or cpu or processor) near10 interfac\$4	USPAT; US-PGPUB	2002/03/05 18:09
8	32743	(memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4	USPAT; US-PGPUB	2002/03/05 18:09
9	17007	interfac\$3 near6 (chip or asic or integrated)	USPAT; US-PGPUB	2002/03/05 18:09
10	2988	(interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)	USPAT; US-PGPUB	2002/03/05 18:10
11	2616	(interfac\$3 near6 (chip or asic or integrated)) near10 memory	USPAT; US-PGPUB	2002/03/05 18:10
12	939	(interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)	USPAT; US-PGPUB	2002/03/05 18:11
13	856	((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME ((interfac\$3 near6 (chip or asic or integrated)) near10 memory)	USPAT; US-PGPUB	2002/03/05 18:13
14	71	((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME ((interfac\$3 near6 (chip or asic or integrated)) near10 memory))	USPAT; US-PGPUB	2002/03/05 18:13
15	63	((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME ((interfac\$3 near6 (chip or asic or integrated)) near10 memory)))	USPAT; US-PGPUB	2002/03/05 18:13
16	63	((microprocessor or cpu or processor) near10 interfac\$4) and ((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME ((interfac\$3 near6 (chip or asic or integrated)) near10 memory))))	USPAT; US-PGPUB	2002/03/05 18:13
17	0	((microprocessor or cpu or processor) near10 interfac\$4) and ((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME ((interfac\$3 near6 (chip or asic or integrated)) near10 memory)))) and (@fd<19990929 or @rlfd<19990929)	USPAT; US-PGPUB	2002/03/05 18:16

18	60	((microprocessor or cpu or processor) near10 interfac\$4) and ((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME ((interfac\$3 near6 (chip or asic or integrated)) near10 memory)))) and (@ad<19990929 or @rlad<19990929) sandorfi-miklos\$.in.	USPAT; US-PGPUB	2002/03/05 18:16
19	7		USPAT; US-PGPUB	2002/03/05 18:36